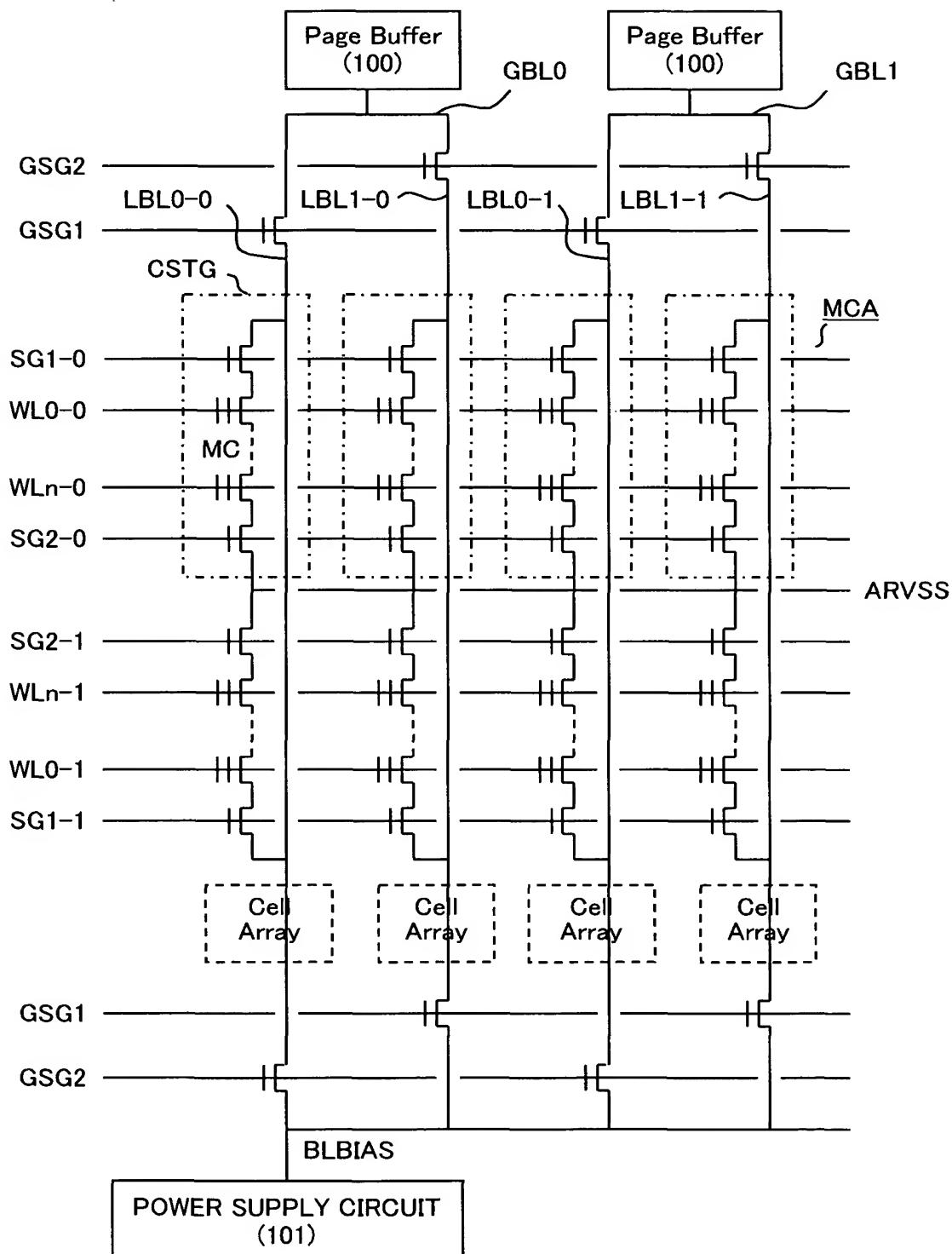
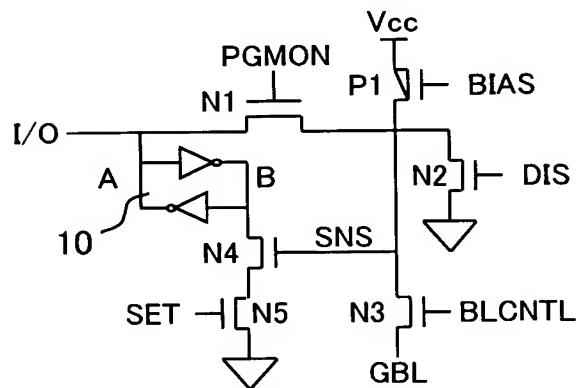


**FIG. 1**  
**PRIOR ART**



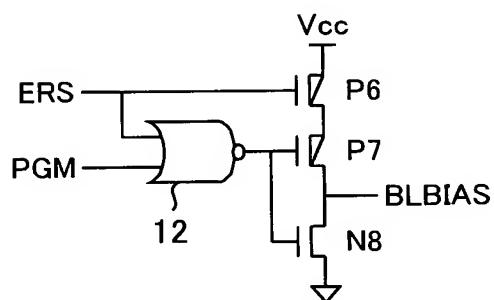
**FIG. 2**

**PAGE BUFFER 100**



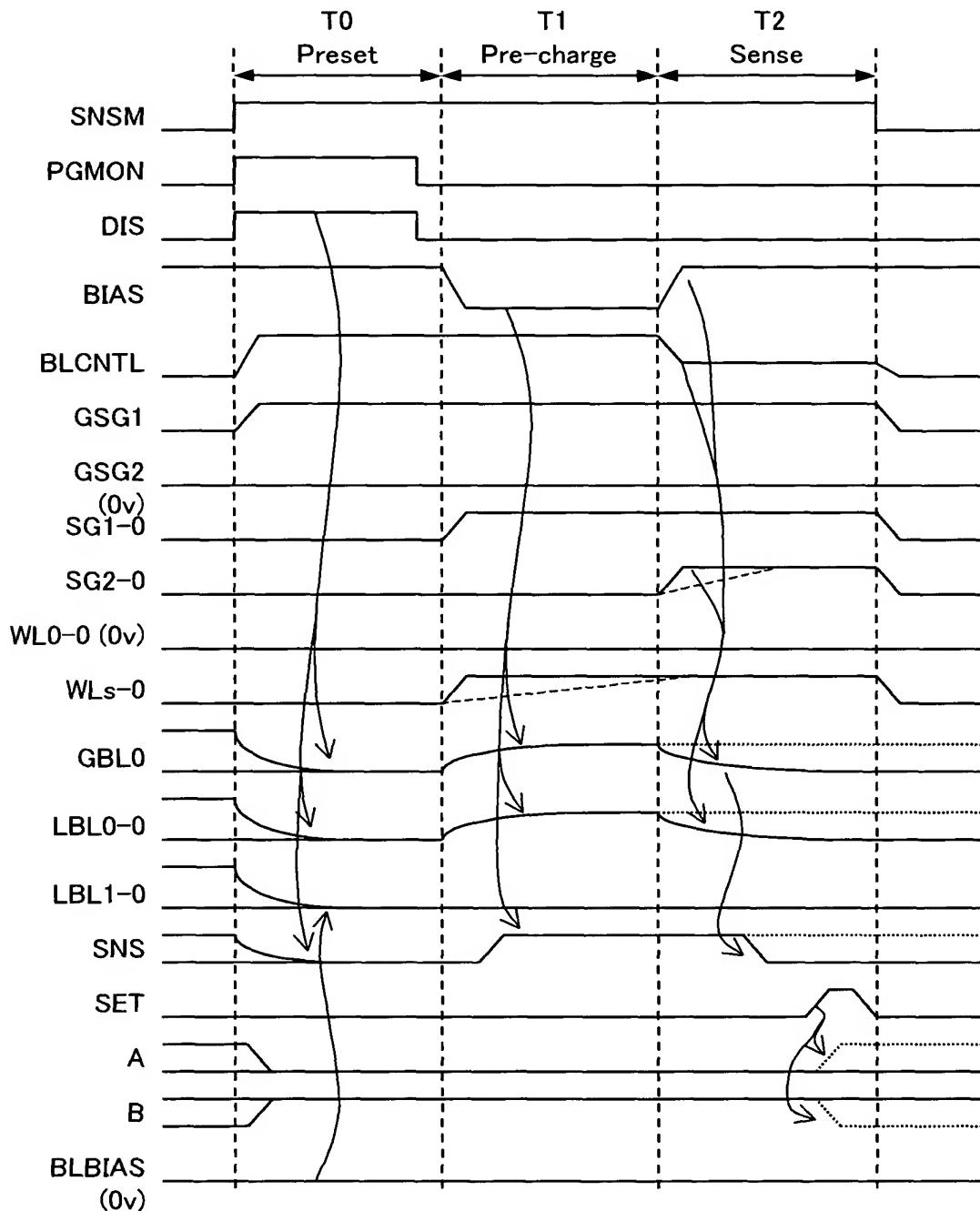
**FIG. 3**

**POWER SUPPLY CIRCUIT 101**



**FIG. 4**

**AC SENSE FOR READ OPERATION OF PRIOR ART**



**FIG. 5**

**AC SENSE FOR PROGRAM VERIFY OF PRIOR ART**

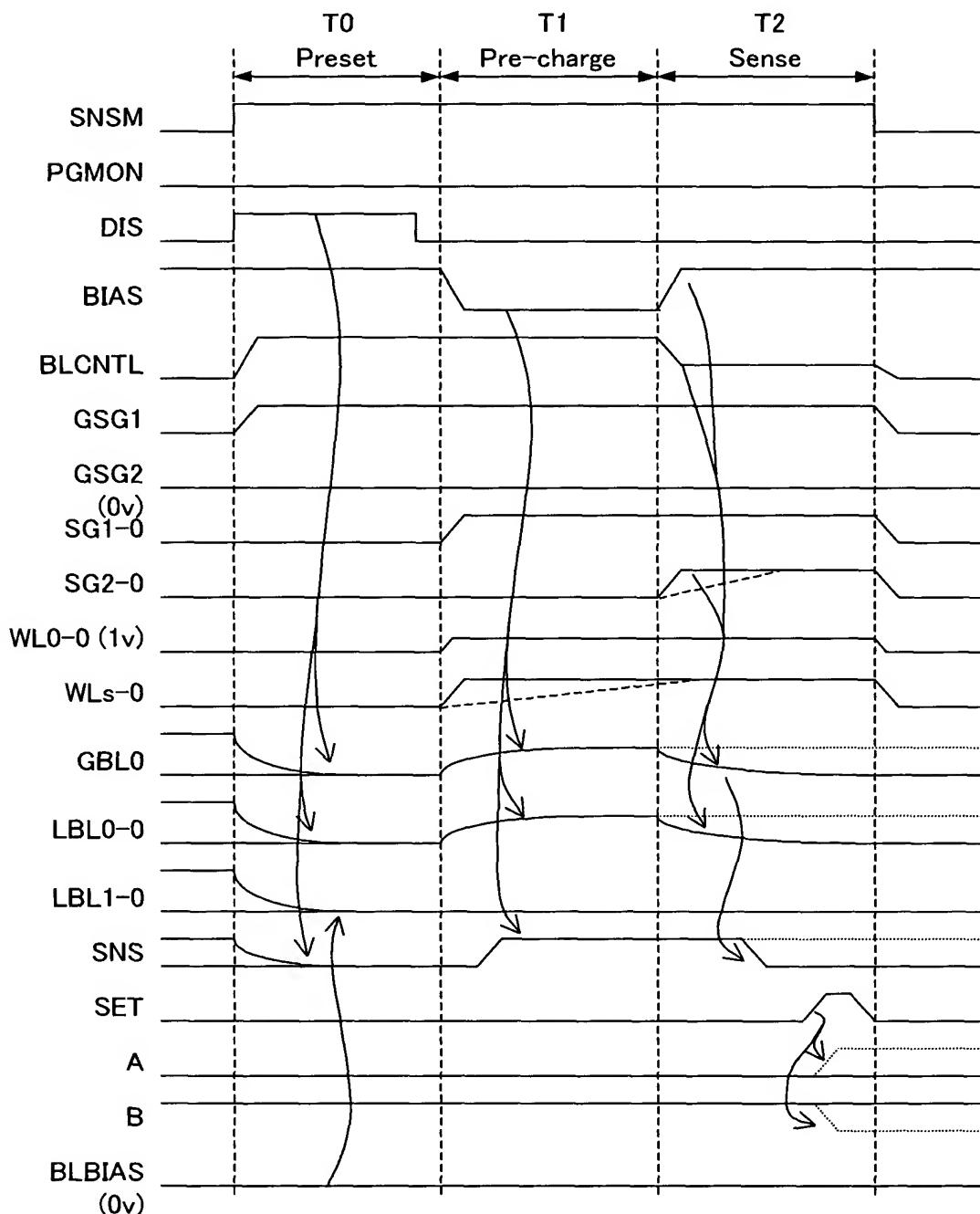
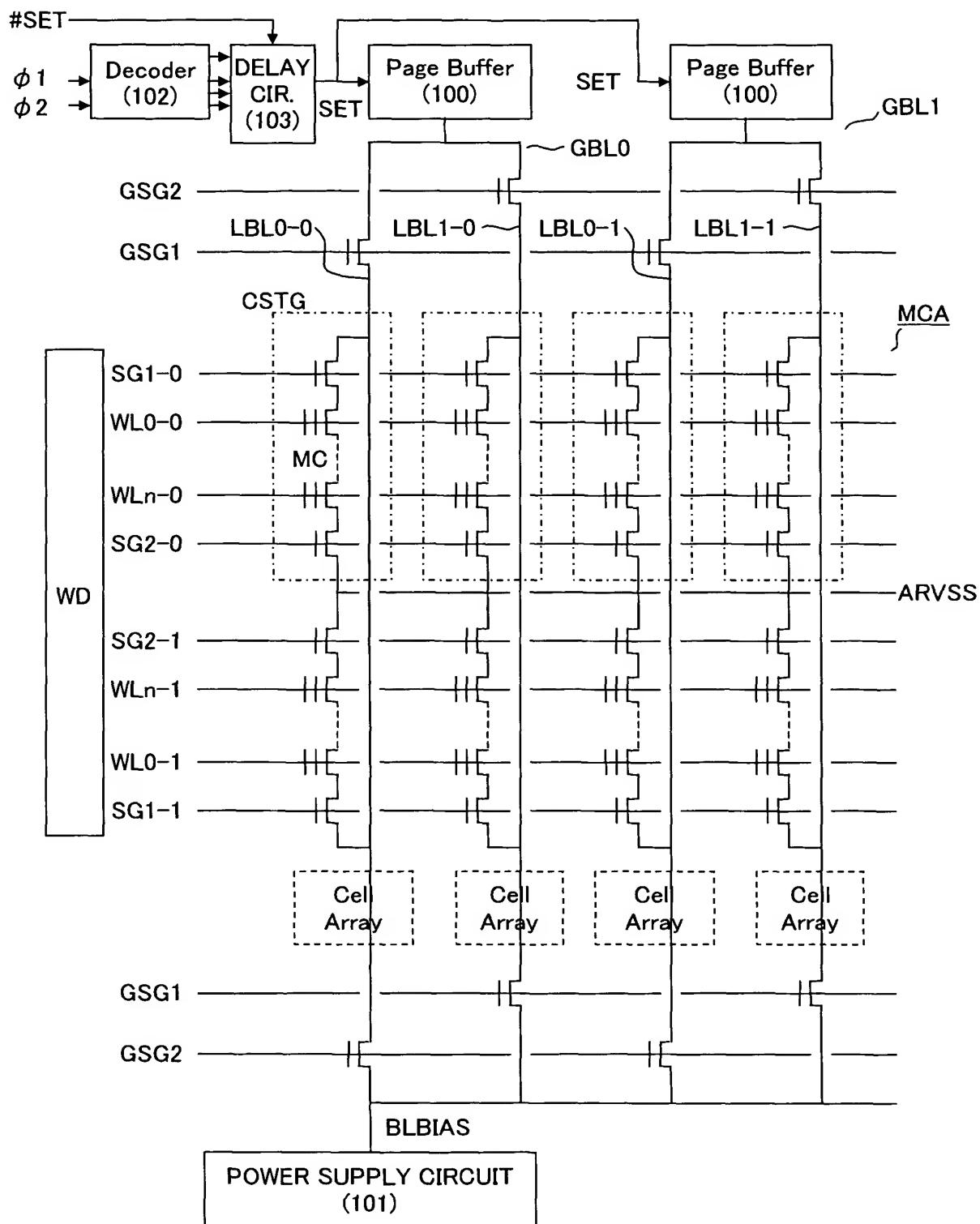
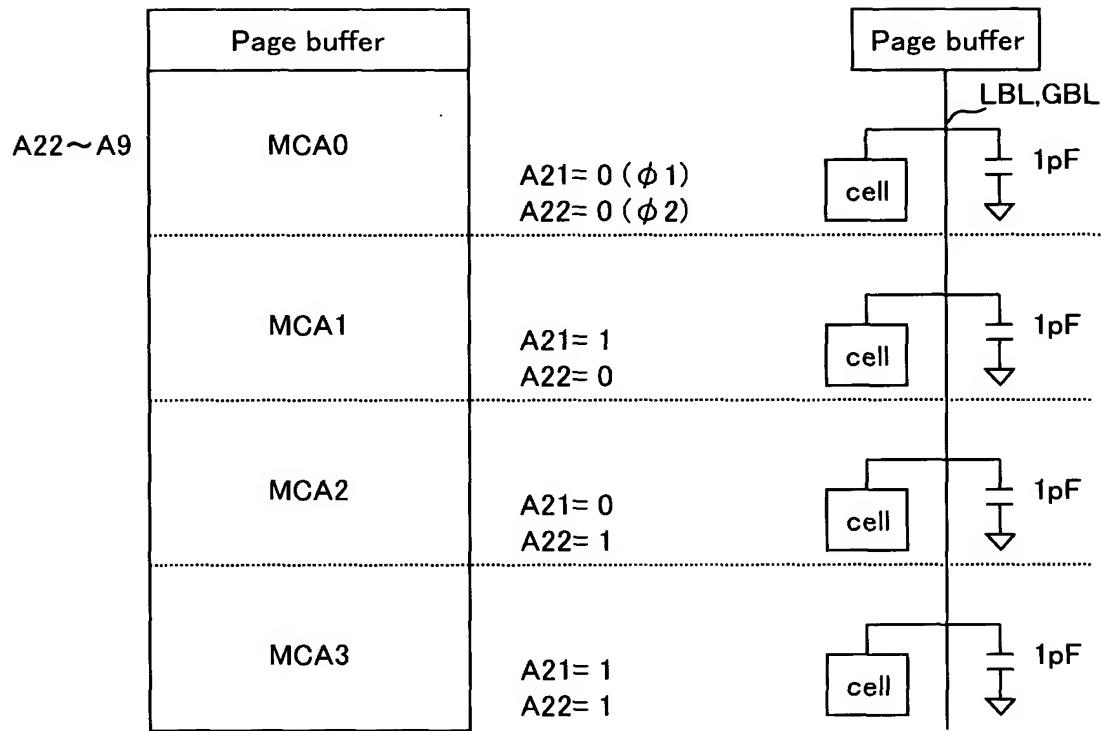


FIG. 6

FIRST EMBODIMENT

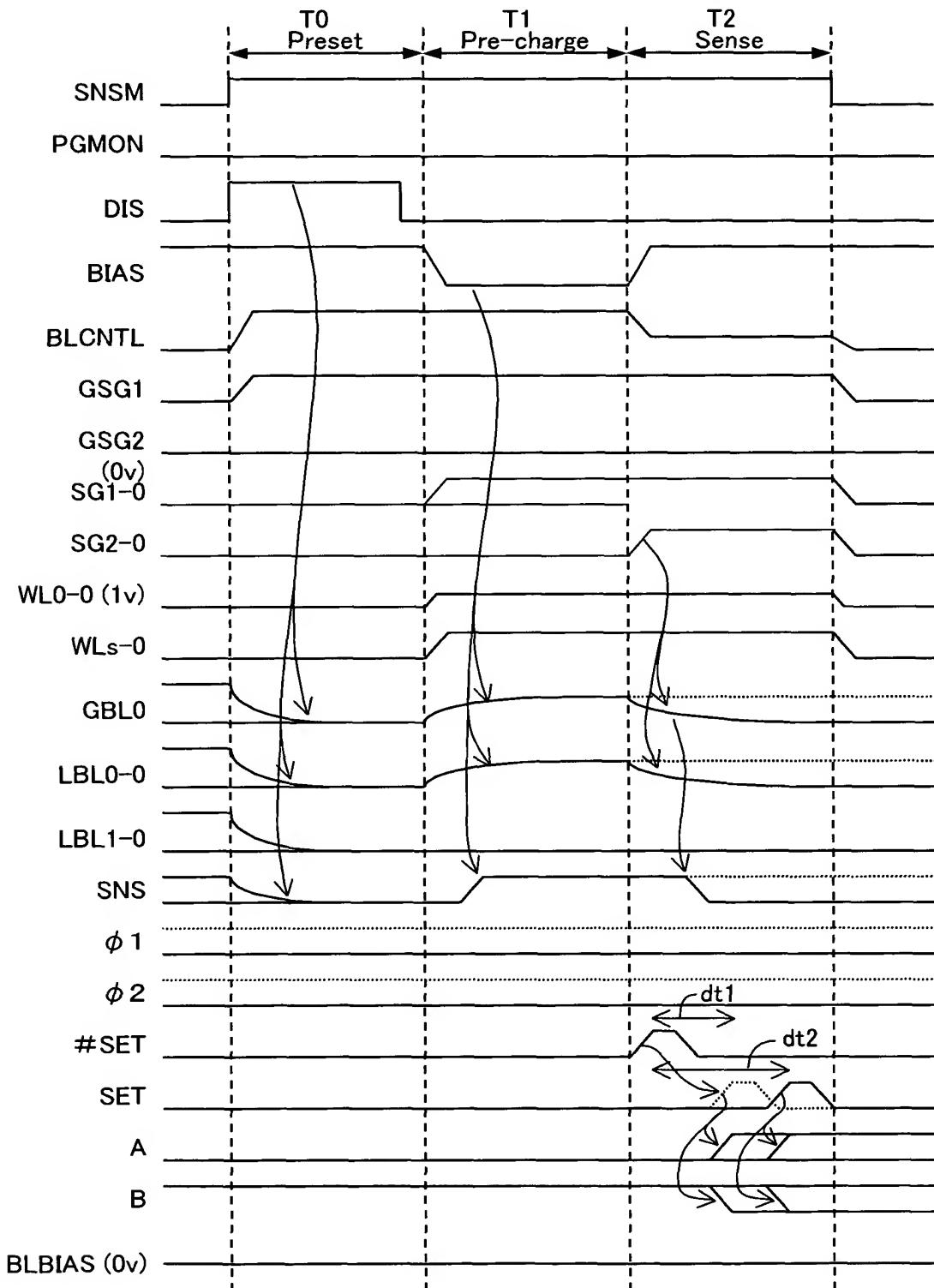


**FIG. 7**  
**FIRST EMBODIMENT**



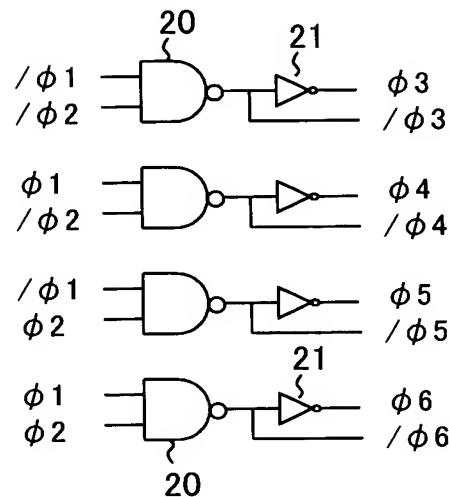
**FIG. 8**

**PROGRAM VERIFY OPERATION OF FIRST EMBODIMENT**



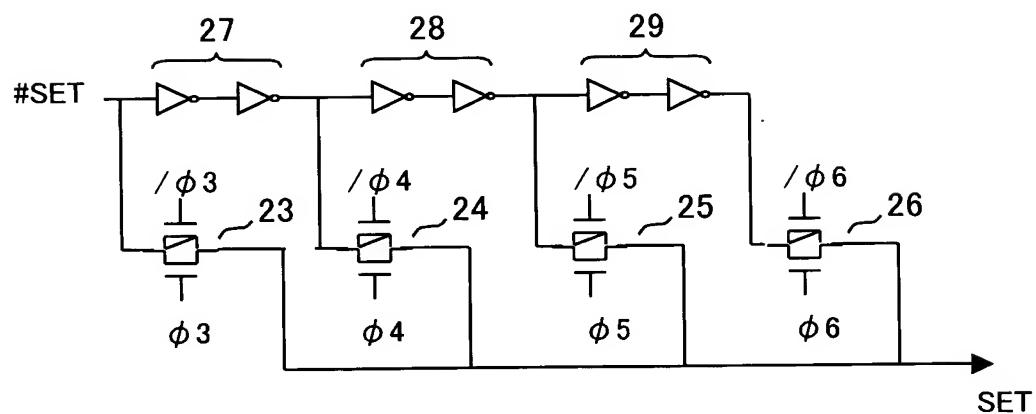
**FIG. 9**

**DEC 102**



**FIG. 10**

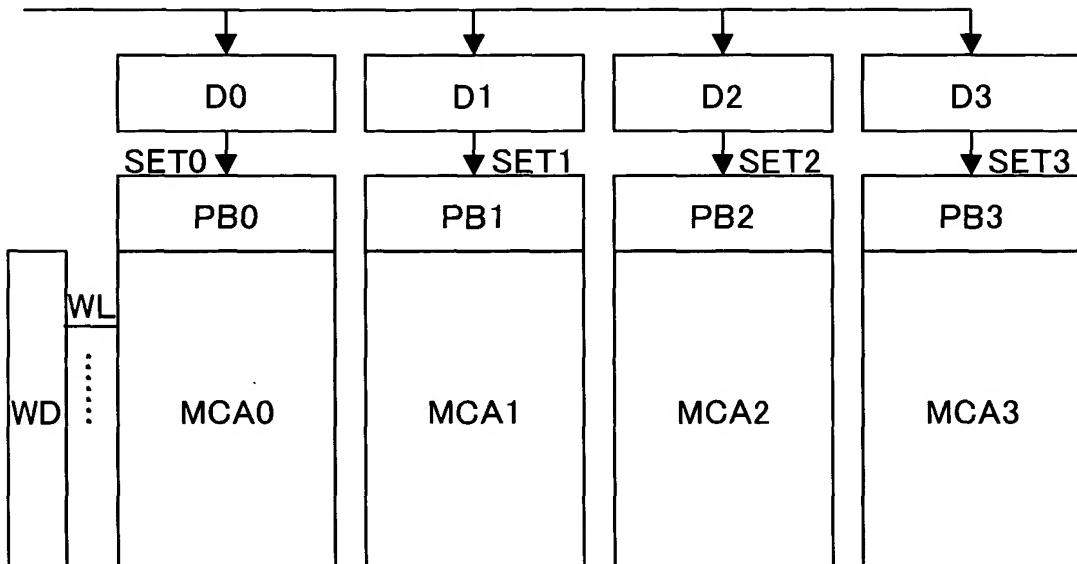
**DEL 103**



**FIG. 11**

**MODIFICATION (1) OF FIRST EMBODIMENT**

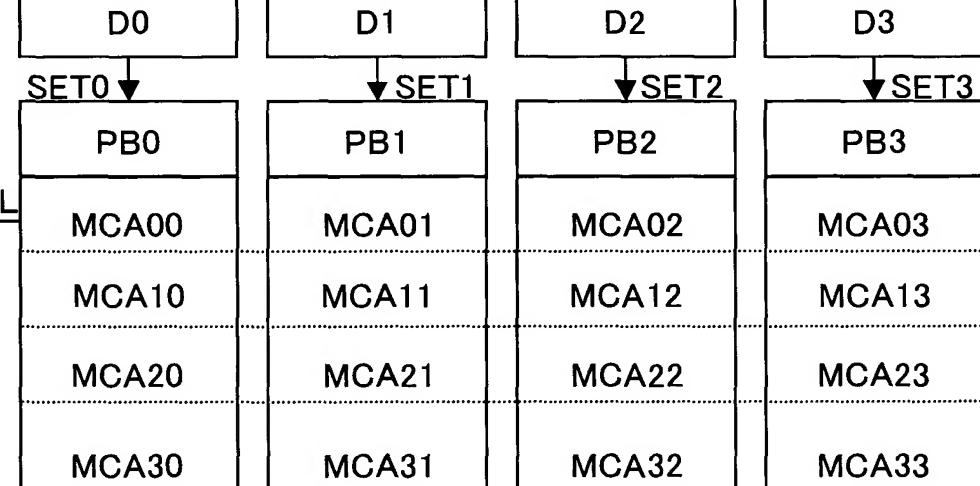
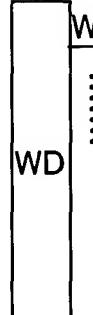
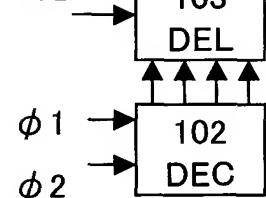
#SET



**FIG. 12**

**MODIFICATION (2) OF FIRST EMBODIMENT**

#SET



**FIG. 13**  
**SECOND EMBODIMENT**

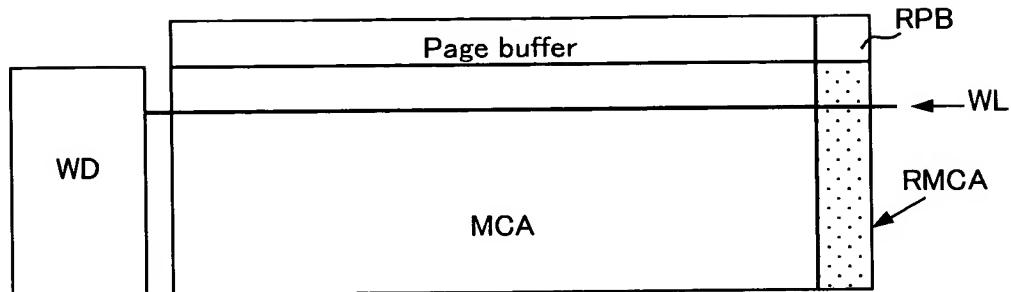


FIG. 14

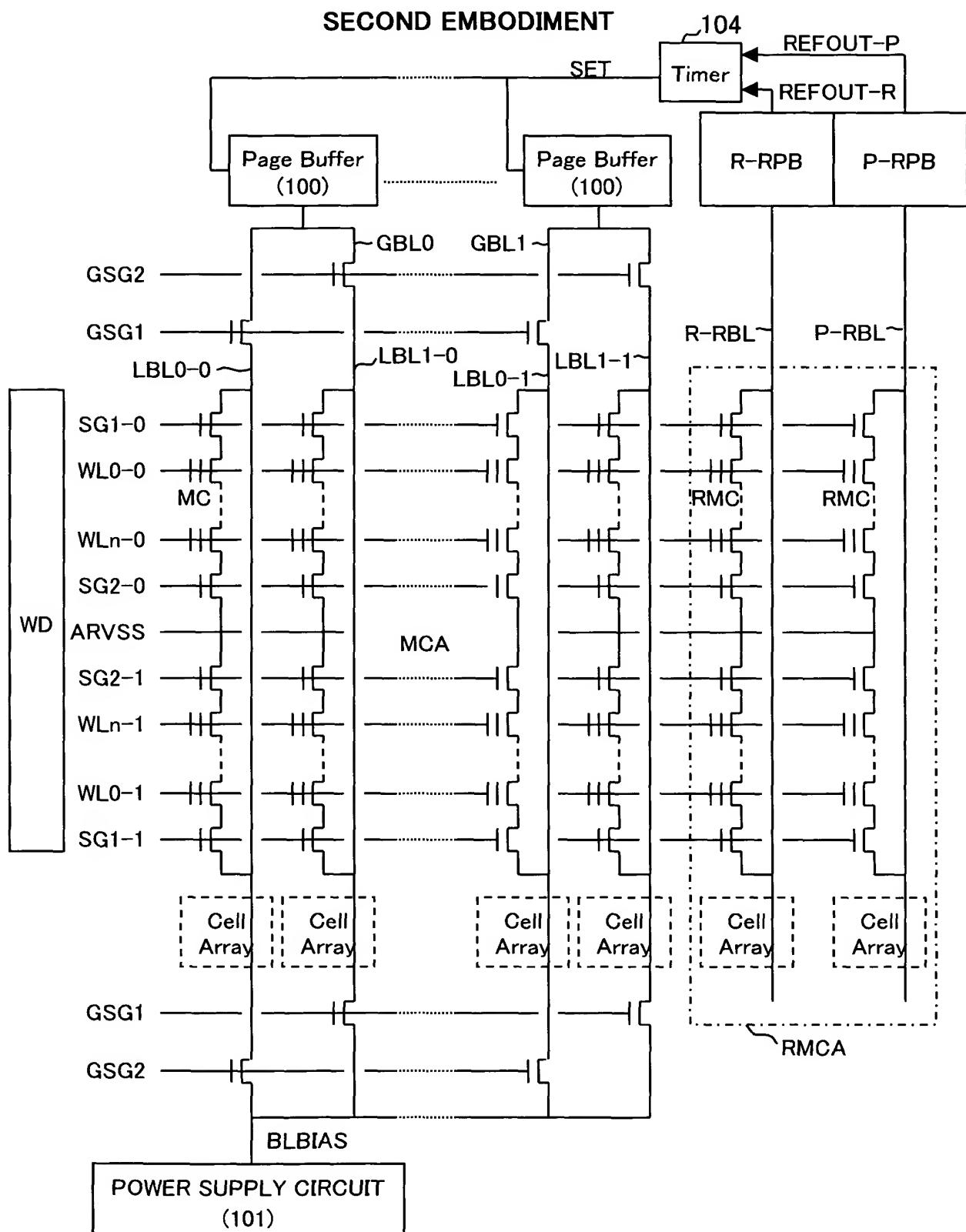


FIG. 15

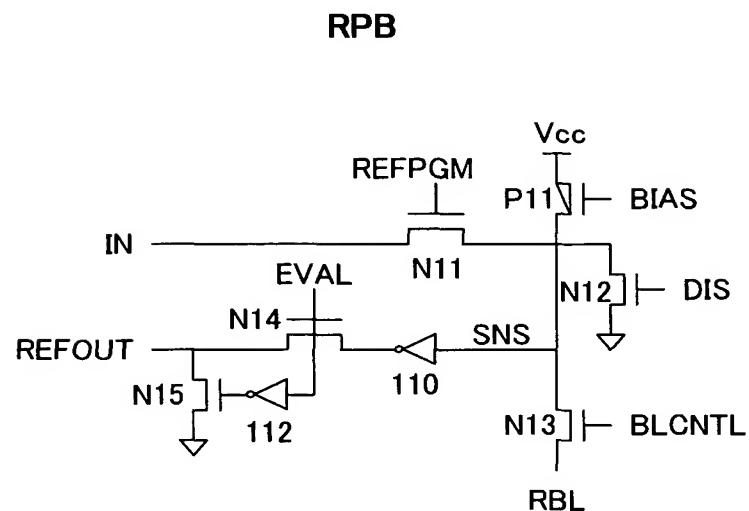
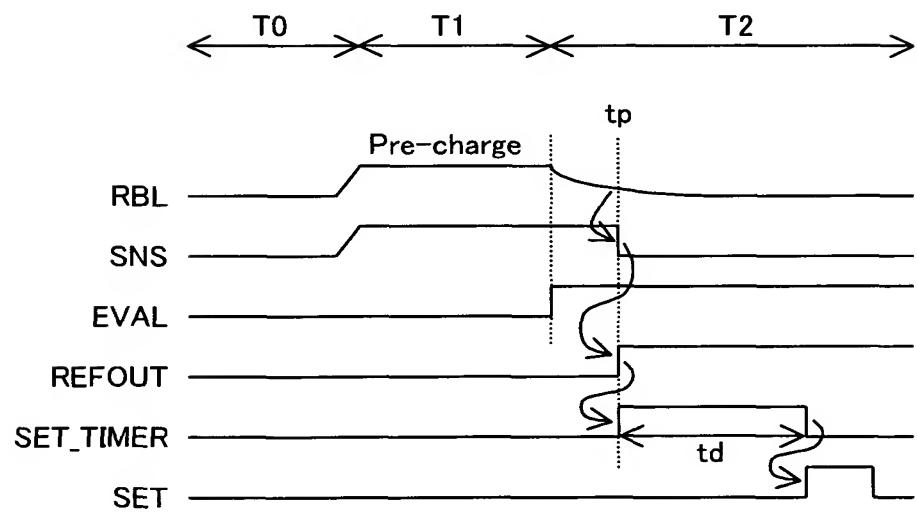
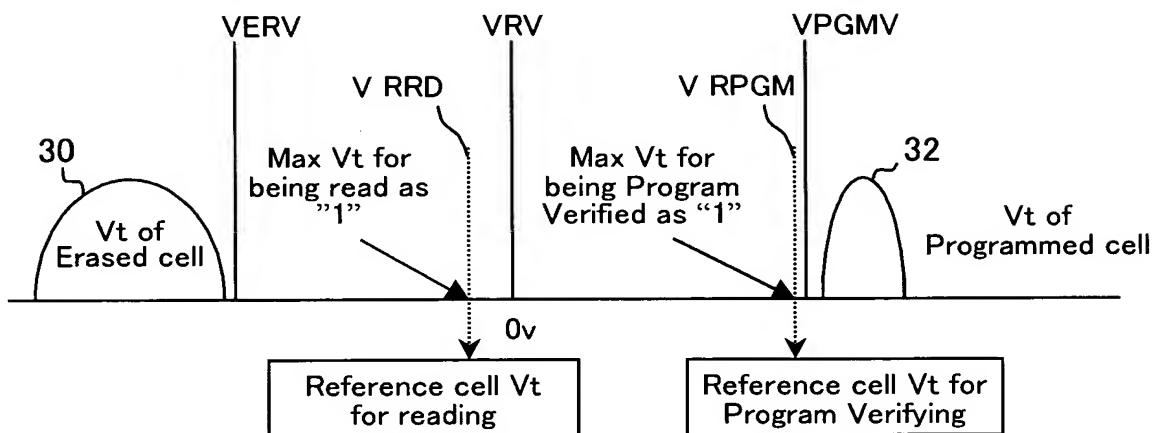


FIG. 16



**FIG. 17**



**FIG. 18**

**THIRD EMBODIMENT**

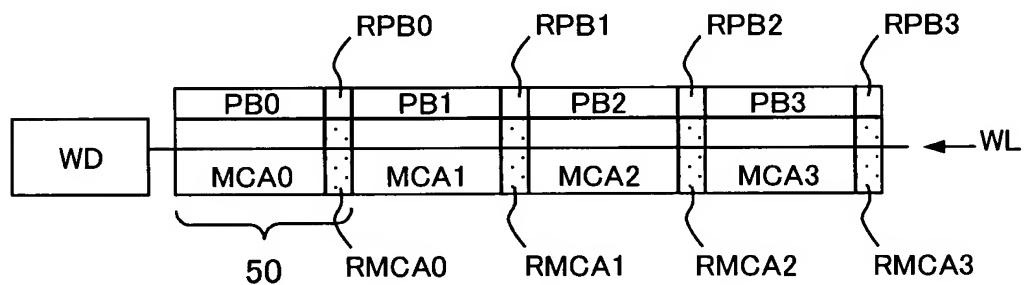


FIG. 19

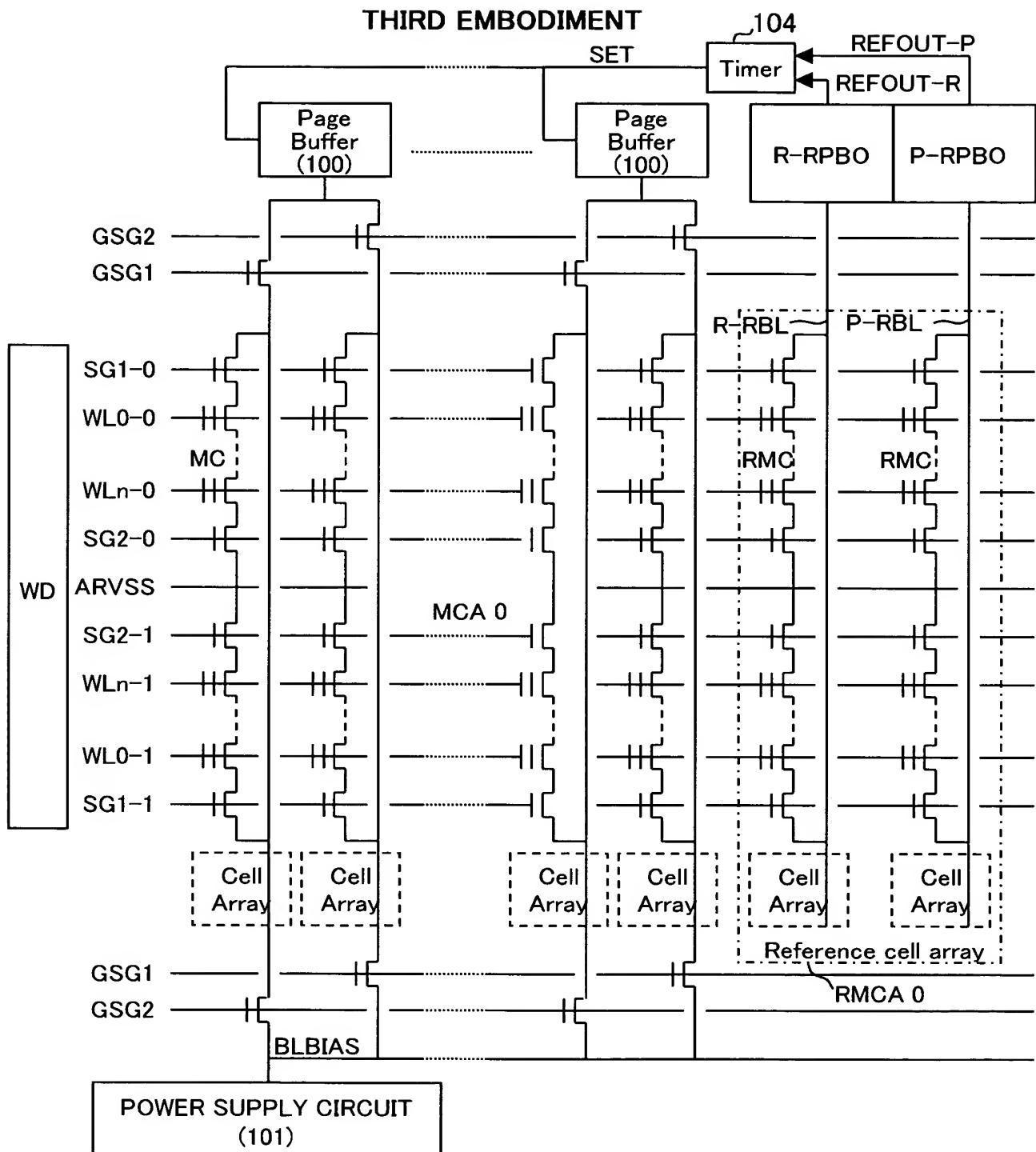


FIG. 20

